

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

( Not for submission under 37 CFR 1.99)

|                        |                 |
|------------------------|-----------------|
| Application Number     | 10786874        |
| Filing Date            | 2004-02-24      |
| First Named Inventor   | Haw-minn Lu     |
| Art Unit               | 2616            |
| Examiner Name          | Wellington Chin |
| Attorney Docket Number |                 |

|        |   |   |                          |
|--------|---|---|--------------------------|
| /Y.Z./ | 1 | PATEL, et al., "Performance of processor-memory interconnections for multiprocessors", IEEE Transactions on Computers, Oct. 1981, pp. 771-780, vol. 30, no. 10, IEEE, US  | <input type="checkbox"/> |
| /Y.Z./ | 2 | GOKE, et al., "Banyan Network for partitioning multiprocessor systems," First Annual International Symposium on Computer Architecture, December 1973, pp. 21-28, ACM Press, US  | <input type="checkbox"/> |
| /Y.Z./ | 3 | KUMAR, et al., "Augmented shuffle-exchange multistage interconnection networks". Computer, June 1987, pp.30-40, vol. 20, no. 6, IEEE, US  | <input type="checkbox"/> |
| /Y.Z./ | 4 | ADAMS, et al., "The Extra Stage Cube: A Fault-Tolerant Interconnection Network for Supersystems," IEEE Transactions on Computers, May 1982, pp. 443-454, vol. 31, no. 5, IEEE, US   | <input type="checkbox"/> |
| /Y.Z./ | 5 | BENES, "Permutation Groups, Complexes, and Rearrangeable Connecting Networks, Bell System Telephone Journal, July 1964, pp. 1619-1640, vol. 44, AT&T, US  | <input type="checkbox"/> |
| /Y.Z./ | 6 | HAMID, et al., "A new fast control mechanism for Benes rearrangeable interconnection network useful for supersystems," Transactions of the Institute of Electronics, Information and Communication Engineers, October 1987, p.997-1008, vol. E70, no. 10, IEICE, Japan. | <input type="checkbox"/> |
| /Y.Z./ | 7 | DUDGEON, et al., "Multidimensional Signal Processing," 1984, pp. 60-111, Prentice Hall, Englewood Cliffs, New Jersey."  | <input type="checkbox"/> |
| /Y.Z./ | 8 | OPPENHIEM, et al., "Digital Signal Processing," 1975, pp. 284-328, Prentice Hall, Englewood Cliffs, New Jersey.   | <input type="checkbox"/> |

If you wish to add additional non-patent literature document citation information please click the Add button

**EXAMINER SIGNATURE**

|                    |             |                 |            |
|--------------------|-------------|-----------------|------------|
| Examiner Signature | /Yong Zhou/ | Date Considered | 08/05/2007 |
|--------------------|-------------|-----------------|------------|

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> See Kind Codes of USPTO Patent Documents at [www.USPTO.GOV](http://www.USPTO.GOV) or MPEP 901.04. <sup>2</sup> Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>3</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>4</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>5</sup> Applicant is to place a check mark here if English language translation is attached.

|  |                        |  |                 |
|--|------------------------|--|-----------------|
| <b>INFORMATION DISCLOSURE<br/>STATEMENT BY APPLICANT</b><br><i>( Not for submission under 37 CFR 1.99)</i> | Application Number     |  | 10786874        |
|  | Filing Date            |  | 2004-02-24      |
|  | First Named Inventor   |  | Haw-minn Lu     |
|  | Art Unit               |  | 2616            |
|  | Examiner Name          |  | Wellington Chin |
|  | Attorney Docket Number |  |                 |

| U.S.PATENTS   |         |                                      |                           |                        |   | Remove   |  |                          |
|---|---------|--------------------------------------|---------------------------|------------------------|---|--|--|--------------------------|
| Examiner Initial*   | Cite No | Patent Number                        | Kind Code <sup>1</sup>    | Issue Date             | Name of Patentee or Applicant of cited Document | Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear |  |                          |
| /Y.Z./  | 1       | 6049542                              |                           | 2000-04-11             | Prasad  |  |  |                          |
| /Y.Z./  | 2       | 5841775                              |                           | 1998-11-24             | Huang   |  |  |                          |
| If you wish to add additional U.S. Patent citation information please click the Add button.               |         |                                      |                           |                        |   | Add  |  |                          |
| U.S.PATENT APPLICATION PUBLICATIONS   |         |                                      |                           |                        |   | Remove   |  |                          |
| Examiner Initial*   | Cite No | Publication Number                   | Kind Code <sup>1</sup>    | Publication Date       | Name of Patentee or Applicant of cited Document | Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear |  |                          |
|   | 1       |                                      |                           |                        |   |  |  |                          |
| If you wish to add additional U.S. Published Application citation information please click the Add button |         |                                      |                           |                        |   | Add  |  |                          |
| FOREIGN PATENT DOCUMENTS  |         |                                      |                           |                        |   | Remove   |  |                          |
| Examiner Initial*   | Cite No | Foreign Document Number <sup>3</sup> | Country Code <sup>2</sup> | Kind Code <sup>4</sup> | Publication Date                                | Name of Patentee or Applicant of cited Document                        | Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear | T5                       |
|   | 1       |                                      |                           |                        |   |  |  | <input type="checkbox"/> |
| If you wish to add additional Foreign Patent Document citation information please click the Add button    |         |                                      |                           |                        |   | Add  |  |                          |
| NON-PATENT LITERATURE DOCUMENTS   |         |                                      |                           |                        |   | Remove   |  |                          |

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**  
( Not for submission under 37 CFR 1.99)

|                        |                 |  |
|------------------------|-----------------|--|
| Application Number     | 10786874        |  |
| Filing Date            | 2004-02-24      |  |
| First Named Inventor   | Haw-minn Lu     |  |
| Art Unit               | 2616            |  |
| Examiner Name          | Wellington Chin |  |
| Attorney Docket Number |                 |  |

| Examiner Initials* | Cite No | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published. | T5                       |
|--------------------|---------|---|--------------------------|
| /Y.Z./             | 1       | CIZEK, et al. "Tradeoff Between Cost and Reliability in Packet Switching MultiStage Interconnection Networks," AFRICON '92 Proceedings., 3rd AFRICON Conference, 22-24 Sept. 1992, pp. 365 -368, IEEE, South Africa (Reprinted US).                             | <input type="checkbox"/> |
| /Y.Z./             | 2       | AGRAWAL, "Testing and Fault-Tolerance of Multistage Interconnection Networks," Computer, Apr. 1982, pp. 41-53, vol. 15, no. 4, IEEE, US   | <input type="checkbox"/> |
| /Y.Z./             | 3       | BHUYAN, et al., "Design and Performance of Generalized Interconnection Networks." IEEE Transactions on Computers, Dec. 1983, pp. 1081-1090, vol. 32, no. 12, IEEE, US   | <input type="checkbox"/> |
| /Y.Z./             | 4       | BLAKE, et al., "Multistage Interconnection Network Reliability," IEEE Transactions on Computers, Nov. 1989, pp. 1600-1603, vol. 38, no. 11, IEEE, US  | <input type="checkbox"/> |
| /Y.Z./             | 5       | CHIN, et al., "Packet Switching Networks for Multiprocessors and Data Flow Computers," IEEE Transactions on Computers, Nov. 1984, pp. 991-1003, vol. 33, no. 11, IEEE, US   | <input type="checkbox"/> |
| /Y.Z./             | 6       | KUMAR, et al., "Failure Dependent Performance Analysis of a Fault-Tolerant Multistage Interconnection Network," IEEE Transactions on Computers, Dec. 1989, pp. 1703-1713, vol. 38, no. 12, IEEE, US   | <input type="checkbox"/> |
| /Y.Z./             | 7       | TZENG, et al., "Realizing Fault-Tolerant Interconnection Network via Chaining," IEEE Transactions on Computers, Apr. 1988, pp. 458-462, vol. 37, no. 4, IEEE, US  | <input type="checkbox"/> |
| /Y.Z./             | 8       | VARMA, et al.; "Fault-Tolerant Routing in Multistage Interconnection Networks," IEEE Transactions on Computers, Mar. 1989, pp. 385-393, vol. 38, no. 3, IEEE, US  | <input type="checkbox"/> |

If you wish to add additional non-patent literature document citation information please click the Add button

**EXAMINER SIGNATURE**

|                    |             |                 |            |
|--------------------|-------------|-----------------|------------|
| Examiner Signature | /Yong Zhou/ | Date Considered | 08/12/2007 |
|--------------------|-------------|-----------------|------------|

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.